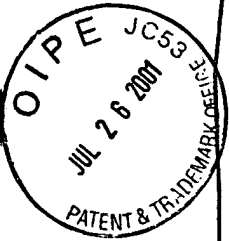


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant(s): Christopher J. de Simone, Lucian M. Hand, Gi Jeong Kim, Seung Mo Kim, Jin An Lee

Assignee: Amkor and ANAM (Jointly)

Title: Nonexposed Heat Sink For Semiconductor Package

Serial No.: 09/513,067

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Examiner: Chuong A. Luu

Group Art Unit: 2825

Docket No.: AB-928 US

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See page 6, lines 21-26.

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action mailed May 9, 2001, Applicants submit the following amendments and remarks.

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IN THE SPECIFICATION

Please amend the specification by replacing various paragraphs, as indicated below. In accordance with 37 CFR § 1.121(b)(1)(iii), Attachment A contains marked up versions of the replacement paragraphs illustrating the newly introduced changes in the specification.

Please replace the paragraph starting on page 1, line 22, with the following replacement paragraph.

Fig. 1 is a cross-sectional view of a conventional semiconductor package 1'. Package 1' includes a semiconductor chip 2 that is attached to a planar upper surface 5a of a heat sink 5' using adhesive 6. Heat sink 5' has a relatively large thickness (e.g., 1 to 3 mm) and may be formed of copper, aluminum, or other materials. A plurality of metal leads 7' surround semiconductor chip 2. Leads 7' are about 0.12 mm to 0.15 mm thick, and thus are much thinner than heat sink 5'. Each lead 7' comprises an encapsulated inner lead 11' and a

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